PROFESSIONAL & CAREER DEVELOPMENT CENTER

In Collaboration with
FEDERAL URDU UNIVERSITY
of Arts, Science & Technology-Islamabad

VLSI Systems & Design

PCDC
TECHNICAL TRAINING

What do you learn and experiment?

a Logic design with MOSFETs and experiment with basic gates and complex functions in DSCH.

b Transmission gate functions, complex gates, integrated circuit layer and MOSFETS. Experimentally implement multiplexers and 4 bit rotation in DSCH.

c CMOS layers, FET arrays and lambda design rules. Experiments in microwind, MOSFET analysis, layout of basic gates using 0.25 micron technology.

d Fabrication of CMOS integrated circuits and elements of physical design. Experiment by microwind in designing layout of complex gates using 0.25 micron technology and implementation of static RAM using 0.12 micron technology.

e Electrical characteristics of MOSFETS and modeling of smsll MOSFETS. Experiments with Modelsim in implementing of complex gates using various levels.

f Electronic analysis and advance techniques in CMOS Logic gates and circuits. Experiment by implementing state machine in modelsim. Completing a simple Project involving FPGA.

Who should attend ?

a All those Electrical engineers, Electronic postgraduates who are involved in R&D organizations and want to update their knowledge.

b Students of 7/8th semester of BSc Elec/telecom Engineering who do not have access to VLSI Design laboratory environment.

c Engineers seeking oversees admissions/ employment in R&D organisations.

Educational background ?

a BSc Engineering in Electrical, Mechatronics, computer systems or Telecom.

b Msc in Electronics

FEE STRUCTURE

- Students
  Rs 5,000/-
- Academic
  Rs 10,000/-
- Professionals
  Rs 15,000/-

DURATION

- 30 Hours
- 3 Days a Week
- 5 Hours a Day

East Side Services (Pvt) Ltd, 2nd Floor, Software Technology Park, F-5/1,Islamabad
Tel: +92-51-2820931, 2878684, 2820965 Fax: +92-51-2820912
Email: info@essbpo.com

Logon to register
www.essbpo.com/pcdc.html